



Docket No. 01-C-050

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): David C. McClure

) Group Art Unit: 2818

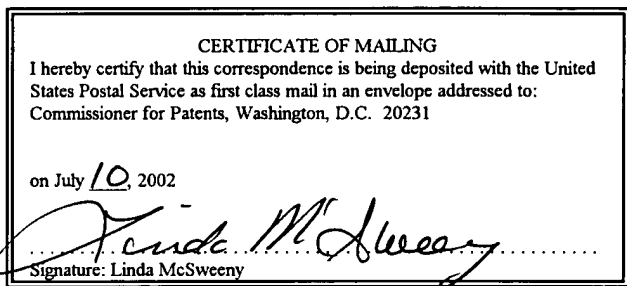
Serial No.: 10/066,182

) Examiner: Not Yet Assigned

Filed: January 31, 2002

For: CIRCUIT AND METHOD FOR TESTING A FERROELECTRIC MEMORY
DEVICE

To the Commissioner for Patents
Washington, D.C. 20231



Dear Sir:

INFORMATION DISCLOSURE STATEMENT

In accordance with Applicant's duty under 37 C.F.R. § 1.56 and 1.97, Applicant hereby submits the attached form PTO-1449 (modified) which lists art cited. The art listed therein, while of some relevance, is not necessarily considered to teach or suggest any aspect of the invention described and claimed in the above-identified patent application. This statement is also not to be construed as a representation that a search has, or has not, been conducted or that no better art exists. Rather, this statement discloses only the best art of which the Applicant is aware.

In considering the art set forth below, it may be noted by the Examiner that certain of the references may contain markings, underlinings or other notations. These markings or notations are not to be construed as drawing the Examiner's attention either to selected parts or away from other parts of the references. Any such markings were either present on the copies of the references

obtained by Applicant, or were made thereon during the study of the references by the Applicant and/or his attorneys.

The Examiner is respectfully requested to consider each of the cited references, indicate such consideration by initialing each reference on the enclosed Form PTO-1449 (modified) and return a copy of the same with the next communication to the Applicant. For the convenience of the Examiner in considering the references, copies of the cited references are enclosed with this communication.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "William F. Esser", with a stylized flourish at the end.

William F. Esser
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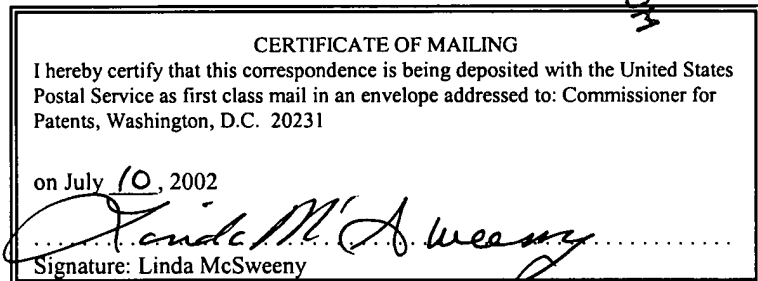
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Re: Applicant(s): David C. McClure
Serial No. 10/066,182
Filing Date: January 31, 2002
For: CIRCUIT AND METHOD FOR TESTING A FERROELECTRIC
MEMORY DEVICE
Docket No.: 01-C-050

Dear Sir:

Transmitted for filing with the Patent and Trademark Office are the following documents for the above-referenced patent application:

1. Information Disclosure Statement
2. Form PTO-1449
3. References A1-A4, B1 and C1
4. Postcard acknowledgment

Please address all communications related to this to:

Lisa K. Jorgenson
STMicroelectronics, Inc.
1310 Electronics Drive
Carrollton, TX 75006-5039

In the event there is an under or over payment, please debit or credit our Deposit Account #10-0447.
This letter is being filed in duplicate to facilitate processing.

Respectfully submitted,

William F. Esser
Registration No. 38,053